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Substitute for form 1449/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 1

Complete if Known

Application Number	Not yet assigned
Filing Date	20 October 2003
First Named Inventor	Mark Beaumont
Art Unit	Not yet assigned
Examiner Name	Not yet assigned
Attorney Docket Number	DB001070-000

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
DDH		US- 6,430,618 B1	08-06-2002	Karger et al.	
DDH		US- 6,421,772 B1	07-16-2002	Maeda et al.	
DDH		US- 6,404,439 B1	06-11-2002	Coulombe et al.	
DDH		US- 6,292,822 B1	09-18-2001	Hardwick	
DDH		US- 6,219,776 B1	04-17-2001	Pechanek et al.	
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DDH		US- 6,078,945	06-20-2000	Hinsley	
DDH		US- 5,966,528	10-12-1999	Wilkinson et al.	
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DDH		US- 5,581,773	12-03-1996	Glover	
DDH		US- 5,535,387	07-09-1996	Matsouka et al.	
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		US-			
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				

Examiner Signature	Date Considered
<i>David J. Johnson</i>	2-27-06

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Sheet 1	1	of	1	Attorney Docket Number	DB001070-000

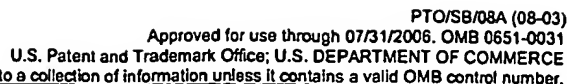
NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
DDH		Daehyun Kim, Mainak Chaudhuri, and Mark Heinrich, Leveraging Cache Coherence in Active Memory Systems, Proceedings of the 16th ACM	
		Int'l Conference on Supercomputing, pages 2-13, New York City, USA, June 2002.	
DDH		Mainak Chaudhuri, Daehyun Kim, and Mark Heinrich, Cache Coherence Protocol Design for Active Memory Systems, Proceedings of the 2002	
		Int'l Conference on Parallel and Distributed Processing Techniques and Applications, pages 83-89, Las Vegas, USA, June 2002.	

Examiner Signature	<i>D. J. Higin</i>	Date Considered	2-27-06
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Sheet	1	of	1
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Complete if Known

Application Number	10/689,257
Filing Date	20 October 2003
First Named Inventor	Mark Beaumont
Art Unit	Not yet assigned
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Attorney Docket Number	DB001070-000

U. S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ *Number ⁴ *Kind Code ⁵ (if known)	MM-DD-YYYY			
004		WO 94/10638 A1	05/11/1994	Marwood		

Examiner Signature	<i>D. J. [Signature]</i>	Date Considered	2-27-06
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